

D G Padhan

Areas of Interest: Time delay systems, control structures and controller design, Relay based identification, Auto-tuning, Control theory applications

Education

- Ph.D., Indian Institute of Technology, Guwahati
- P.G., Maulana Azad National Institute of Technology, Bhopal
- U.G., The Institution of Engineers (India)

Academic/Industrial Experience

1. Professor, Electrical and Electronics Engineering, GRIET Hyderabad, 2014-Present
2. Professor, Electrical and Electronics Engineering, GNITC Hyderabad, 2013-2014
3. Professor, Electrical and Electronics Engineering, GMRIT Rajam, June 2013-Dec 2013
4. Associate Professor, Electrical and Electronics Engineering, GMRIT Rajam, 2008-2013
5. Assistant Professor, Electrical and Electronics Engineering, GMRIT Rajam, Aug 2003-2008
6. Assistant Professor, Electrical and Electronics Engineering, IACR Engineering College, March 2003- Aug 2003

Selected Publications

1. D. G. Padhan and S. Majhi (2013), A New Control Scheme for PID Load Frequency Controller of Single-area and Multi- area Power Systems, ISA transactions, Elsevier, 52(2), 242– 251 (Citation-63, Impact factor-3.394)
2. D. G. Padhan and S. Majhi (2013), Enhanced Cascade Control for a Class of Integrating Processes with Time Delay, ISA transactions, Elsevier, 52 (1), 45-55 (Citation-32, Impact factor-3.394)
3. D. G. Padhan and S. Majhi (2012), Modified Smith Predictor based Cascade Control of Unstable Time Delay Processes, ISA transactions, Elsevier, 51 (1), 95-104 (Citation-43, Impact factor-3.394)
4. D. G. Padhan and S. Majhi (2012), An Improved Parallel Cascade Control Structure for Processes with Time Delay, Journal of Process Control, Elsevier, 22 (5), 884-898 (Citation-14, Impact factor-2.7)
5. D. G. Padhan and S. Majhi (2011), Modified Smith Predictor and Controller for Time Delay Processes, IET Electronics letters, IEEE Xplore 47 (17), 959-961 (Citation-16, Impact factor-1.15)

Workshops Conducted/Attended

1. A one week Workshop on 'Methods of Improving Teaching Techniques' conducted by National Institute of Technical Teachers Training & Research, Chennai at GMRIT, Rajam, from 24-11-2003 to 29-11-2003.
2. A five day STTP program on "Advances in DSP" organized by Dept. of EEE & Dept. of ECE, GMRIT, Rajam from 26-03-2007 to 30-03-2007.
3. A one week FDP on 'Guide to a Passionate Teacher' conducted by Department of Career Guidance and Counseling at GRIET, Hyderabad during 29th June-04th July 2015.
4. A two day workshop on "IEEE Smart Tech Workshop-2015 (Smart City)" organized by IEEE Bangalore section during 25th -26th September 2015.
5. A five day FDP on "NI LabVIEW Core 1 & Core 2" conducted by National Instruments at GRIET, Hyderabad during 4-8 January 2016
6. A four week AICTE approved FDP by IIT Bombay on - Use of ICT in Education for Online and Blended Learning during May 2nd, 2016 to July 10th, 2016.
7. A Two Week ISTE STTP on "Electric Power System" by IIT Kharagpur during 10th July to 15th July 2017.
8. Convener, International Conference on Innovation in Electrical and Electronics Engineering(ICIEEE-2014), conducted on 5-6 September 2014 at GNITC, Hyderabad
9. Convener, SERB (DST) sponsored National Workshop on "Advances in Embedded Systems" conducted on 11-12 April 2015 at GNITC, Hyderabad